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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s): Craig Nemecek

Application No.: 09/992,076

Group Art Unit:

Filed: 11/13/01

Examiner:

Title: A SYSTEM AND A METHOD FOR CHECKING LOCK-STEP CONSISTENCY BETWEEN AN
IN-CIRCUIT EMULATION AND A MICROCONTROLLER

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
<i>[Signature]</i>	A	6,460,172	10/01/02	Insenser Farre et al.	716	17	06/21/00

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	B							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	C	
Examiner	<i>[Signature]</i>	
	Date Considered <i>3/14/2006</i>	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.